

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants : Naofumi NAKAMURA et al.  
Serial No. : 10/752,642  
For : SEMICONDUCTOR DEVICE AND METHOD OF  
FABRICATING THE SAME  
Filed : January 7, 2004 Do Not Enter  
Examiner : Chris C. Chu c.c.  
Art Unit : 2815 9/21/08  
Confirmation No. : 4109

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**ON September 8, 2008**

**RESPONSE TO FINAL OFFICE ACTION**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Final Office Action dated June 6, 2008, please consider the following remarks.

**Listing of the Claims** begins on page 2 of this paper.

**Remarks/Arguments** begin on page 6 of this paper.